

# Claims

- [c1] 1. A driving circuit of a display, for converting a first display data into an image driving signal, comprising:  
a first display data;  
a gamma voltage generator, for providing a plurality of first gamma voltages;  
a plurality of first buffers, wherein each of the first buffers is provided for receiving each of the first gamma voltages to generate a second gamma voltage respectively; and  
a converter, for receiving the second gamma voltages and the first display data to output an image driving signal by selecting one of the second gamma voltages according to the first display data.
- [c2] 2. The driving circuit of claim 1, wherein the gamma voltage generator further receives a plurality of gamma adjusting voltages in order to generate the first gamma voltages corresponding to the gamma adjusting voltages.
- [c3] 3. The driving circuit of claim 1, wherein the first display data comprises a set of data in parallel.

- [c4] 4. The driving circuit of claim 1, wherein the converter comprises a D/A converter.
- [c5] 5. The driving circuit of claim 1, further comprising:  
a second display data;  
a horizontal synchronous signal;  
a shift register, for receiving the second display data to generate a third display data; and  
a second buffer, for receiving the third display data and the horizontal synchronous signal to latch the third display data according to a timing of the horizontal synchronous signal and to generate the first display data.
- [c6] 6. The driving circuit of claim 5, wherein the second display data comprises a set of data in serial.
- [c7] 7. The driving circuit of claim 5, wherein the second buffer comprises a line buffer.
- [c8] 8. A flat panel display, comprising:  
a display panel, having a plurality of pixels;  
a timing controller, for outputting a scanning signal, a first display data and a horizontal synchronous signal;  
a set of gate driving circuits, having a plurality of gate drivers for receiving the scanning signal; and  
a set of source driving circuits having a plurality of source drivers, wherein each of the source drivers con-

verts a first display data into an image driving signal according to a timing of the horizontal synchronous signal, wherein the source driver comprises:

a gamma voltage generator, for providing a plurality of first gamma voltages;

a plurality of first buffers, wherein each of the first buffers is provided for receiving each of the first gamma voltages to generate a second gamma voltage respectively; and

a converter, for receiving the second gamma voltages and the first display data to output the image driving signal by selecting one of the second gamma voltages according to the first display data.

[c9] 9. The flat panel display of claim 8, wherein the gamma voltage generator further receives a plurality of gamma adjusting voltages in order to generate the corresponding first gamma voltages.

[c10] 10. The flat panel display of claim 8, wherein the first display data comprises a set of data in parallel.

[c11] 11. The flat panel display of claim 8, wherein the converter comprises a D/A converter.

[c12] 12. The flat panel display of claim 8, further comprising: a second display data;

a horizontal synchronous signal;  
a shift register, for receiving the second display data to generate a third display data; and  
a second buffer, for receiving the third display data and the horizontal synchronous signal to latch the third display data according to a timing of the horizontal synchronous signal and to generate the first display data.

[c13] 13. The flat panel display of claim 12, wherein the second display data comprises a set of data in serial.

[c14] 14. The flat panel display of claim 12, wherein the second buffer comprises a line buffer.

[c15] 15. The flat panel display of claim 8, wherein the flat panel display comprises a liquid crystal display (LCD).

[c16] 16. The flat panel display of claim 8, wherein the flat panel display comprises an amorphous silicon LCD.

[c17] 17. The flat panel display of claim 8, wherein the flat panel display comprises a low temperature poly-silicon LCD.

[c18] 18. The flat panel display of claim 8, wherein the flat panel display comprises an organic light emitting diode display.

[c19] 19. The flat panel display of claim 8, wherein the flat

panel display comprises a reflective LCD.

[c20] 20. The flat panel display of claim 19, wherein the reflective LCD comprises a liquid crystal on silicon.